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FIG. 1

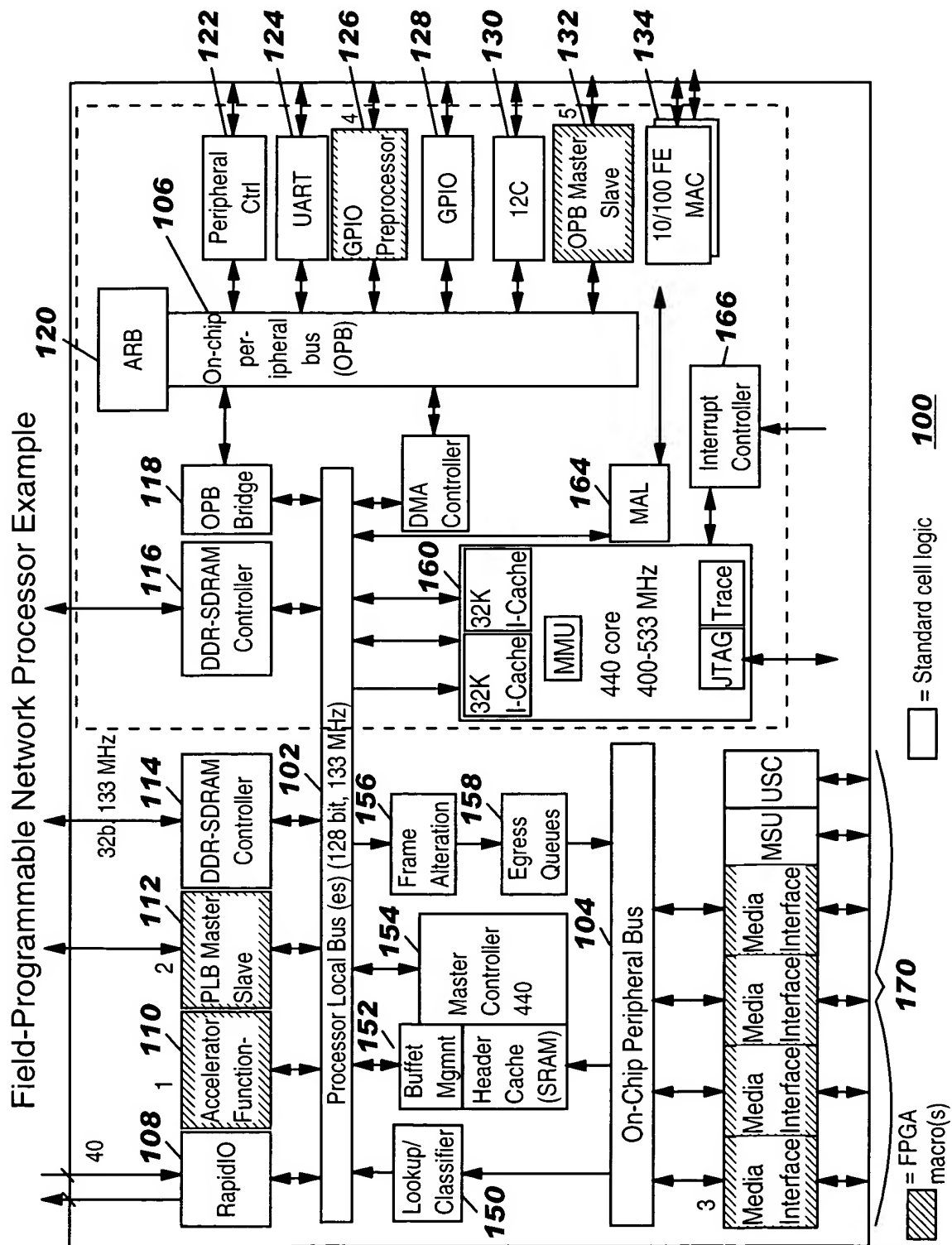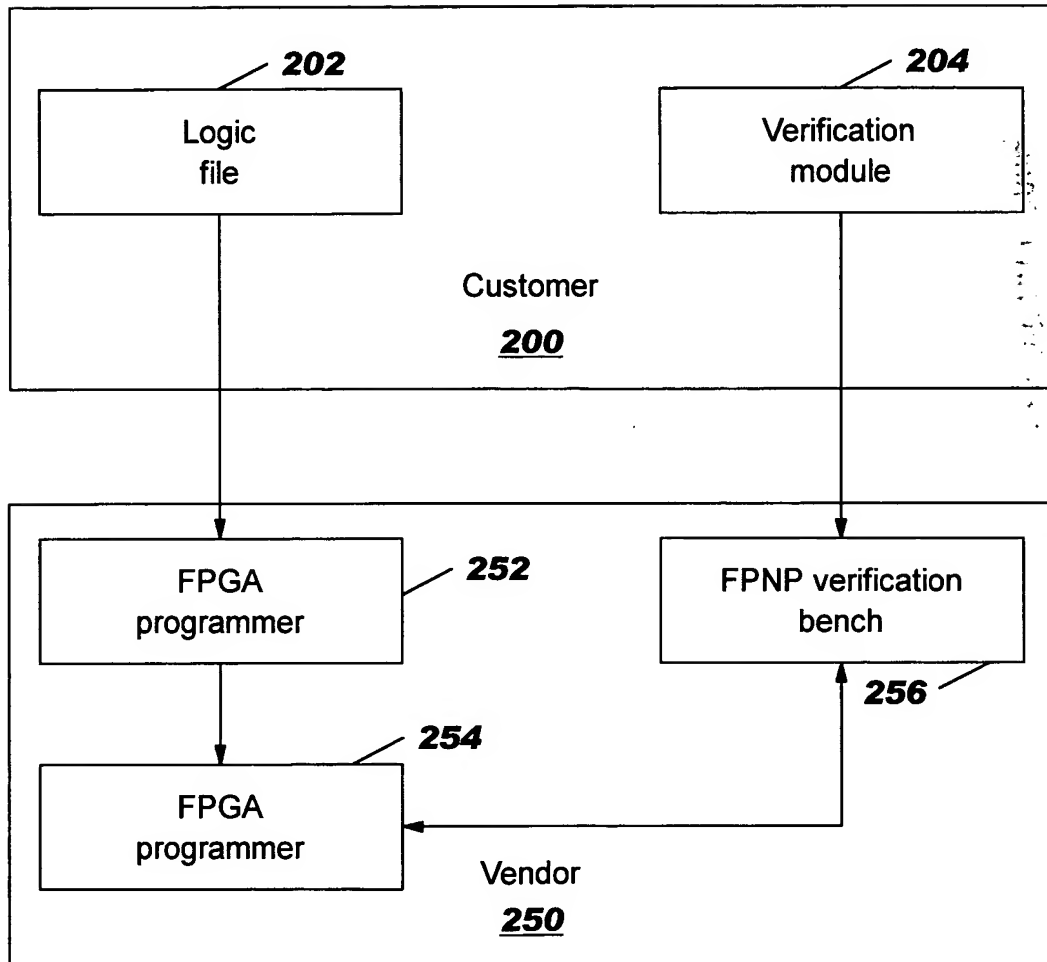## FPGA blocks for User-specific functions and I/O interfaces

Field-Programmable Network Processor Example

= Standard cell logic

= FPGA macro(s)

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FIG. 2

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FIG. 3

Send logical description and verification module by customer to vendor — *302*

Customize the FPNP in accordance with logical description and FPGA programming techniques by vendor — *304*

Verify the resulting network processor by vendor using verification module and verification bench — *306*